RESUME

Raghavendra KR

raghavendra.kaundinya@gmail.com +91-9901538538 Current Address Dept. of Computer Science &

Automation,

Indian Institute of Science,

Bangalore - 12,

India

Permanent #75,

Address Telecom Layout 1st Stage,

Bogadi 1st Stage,

Mysore – 9, India

Date of Birth 14th Dec 1980

Languages Kannada, Hindi, English

I am doing PhD in Dept. of Computer Science and Automation, Indian Institute of Science, Bangalore under the guidance of Prof Deepak D'Souza. My research area is Verification of Information Security.

Research publications

- 1. An Automata Based approach for verifying information flow properties (with Deepak D'Souza and Barbara Sprick) appeared in Proceedings of Automated Reasoning of Security Protocol Analysis (ARSPA) 2005, Lisbon
- Checking Unwinding Conditions for Finite state systems (with Deepak D'Souza) appeared in Proceedings of VERIFY 2006, Seattle
- 3. On the Decidability of Model-Checking Information Flow Properties (with Deepak D'Souza, Barbara Sprick, Raveendra Holla and Janardhan Kulkarni) appeared in Proceedings of International Conference on Information Systems and Security (ICISS) 2008, Hyderabad
- 4. Model Checking Trace Based Information Flow Properties (with Deepak D'Souza, Barbara Sprick and Ravindra Holla) appeared in Journal of Computer Security 2011
- 5. SEAL: A Logic programming framework for specifying and verifying access control models (with Prasad N) to appear in Symposium on Access Control Models And Technologies (SACMAT) 2011, Innsbruck

Internships / Collaboration

Research Lab	Topic	When
Intel Systems Research Center, Bangalore	High assurance development of operating systems	May 15 th 2006 to Aug 15 th 2006
Microsoft Research, Bangalore	Automatically verifying Labelled based Access Control Models	Feb 4 th 2008 to May 30 th 2008 and Jan 5 th 2009 to Jan30th 2009
Microsoft Research, Bangalore	Logics for authorization in distributed systems	Ongoing

Important Courses Done - Automata Theory, Discrete Mathematics, Algorithms, Automated Verification, Program Analysis, Mathematical Logic and Theorem Proving.

Academic details

Examination	Institution	Graduated on	Result
M.Sc (Engg)	Dept CSA, IISc, Bangalore	Jan 2007	CGPA of 6.4 out of 8
BE (Comp. Sci.)	SJCE, Mysore, VTU	Jul 2002	81.81 %
Pre University College	Karnataka State Board, Sarada Vilas, Mysore	Apr 1998	91.50 %
Secondary School	Karnataka State Board, Sarada Vilas, Mysore	Apr 1996	89.28 %

Before my Masters' course, I worked for Ittiam Systems Pvt. Ltd., Bangalore for 2 years. Ittiam Systems (http://ittiam.com) is a DSP technology company. The major projects in which I was involved are

- 1. GSM AMR Narrow Band speech codecs' implementation on processors like Videocore, Lexra for mobile phone and video conferencing solutions
- 2. MPEG 1 / 2 Layer 1 / 2 / 3 encoders on ARC A5 and A6 processors for a demo demonstrating the capabilities of ARC configurable cores.
- 3. MPEG 4 AAC encoder on ARM processors for audio / video solutions.
- 4. G.728 speech codec implementation on ARC processor.

The projects undertaken during Bachelors' programme are

- 1. Developing Graphics Editor in C for DOS operating system
- 2. Developing Text Editor for Linux using ncurses library
- 3. A networking project "Simulation of Routing Algorithms" in C on Linux
- 4. Automated Face Recognition System in C on DOS
- 5. MP3 Decoder on C54X processor (Bachelors' Dissertation)

I presented a paper on "Decoding of Vocal Tunes into Seven basic elements" in IEEE inter collegiate seminar and secured the shared first prize. I was also an executive member of Computer Society of India (CSI) in SJCE.

I have good programming skills in C, C++, Java, Haskell, F# on Windows and Linux.

Miscellaneous

I like to read books on current affairs, biographies, history, philosophy, poetry and sanskrit literature. I also like to watch science based futuristic and creative movies. I enjoy playing cricket, football, volley ball, badminton. Occasionally I write short poems in kannada